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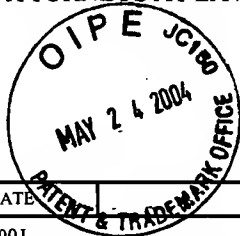
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7590

02/20/2004

LERNER AND GREENBERG, P.A.
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RALPH E. LOCHER	(Depositor's name)
REG. NO. 41,947	(Signature)
May 20, 2004	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/997,983	11/29/2001	Heinz Hoenigschmid	MUH-12041	4244

TITLE OF INVENTION: INTEGRATED MAGNETORESISTIVE SEMICONDUCTOR MEMORY AND FABRICATION METHOD FOR THE MEMORY

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	05/20/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
PHAN, TRONG Q	2818	365-158000

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- 1 Laurence A. Greenberg
- 2 Werner H. Stemer
- 3 Ralph E. Locher

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Infineon Technologies AG

Muenchen, Germany

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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(Authorized Signature) **RALPH E. LOCHER** (Date) **5/20/04**
REG. NO. 41,947

05/25/2004 JADD02 00000005 09997983

01 FC:1501 1330.00 OP

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